Form PTO-1449 (Modified)	Atty Docket No.: TI-33257	Serial No.: ~~Unknown~~
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Applicant: Richard Kane Stair, *et al.*	
	Filing Date: Herewith	Group: ~~Unknown~~ 2816

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
M	AA	5	9	5	2	8	7	4	09/14/1999	Manaresi, et al.	327	541	12/19/1995
M	AB	6	0	8	4	4	7	5	07/04/2000	Rincon-Mora	330	255	10/06/1998

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AC							

OTHER ART(Including Author, Title, Date, Pertinent Pages, etc.)

M	AD		X. Zhou, *et al.*; "Threshold Voltage Definition and Extraction for Deep-Submicron MOSFET's", *Solid-State Electronics*, Revised October 17, 2000, p. 1-3
M	AE		A. Ortiz-Conde, *et al.*; "A New Approach to Extract the Threshold Voltage of MOSFET's", *IEEE Transactions on Electron Devices*, vol. 44, no. 9, September 1997, p. 1523-1528
M	AF		G. Alfonso Rincon-Mora; "Active Capacitor Multiplier in Miller-Compensated Circuits", *IEEE*, 1999, p. 1-15
M	AG		C. Galup-Montoro, *et al.*; "MOSFET Threshold Extraction from Voltage-Only Measurements", *Electronics Letters*, vol. 30, no. 17, August 18, 1994, p. 1458-1459
M	AH		N. Manaresi, *et al.*; "MOSFET Threshold Extraction Circuit", *Electronics Letters*, vol. 31, no. 17, August 17, 1995, p. 1434-1435
M	AI		J. Ramirez-Angulo, *et al.*; "Low-Voltage CMOS Op-Amp with Rail-to-Rail Input and Output Signal Swing for Continuous-Time Signal Processing Using Multiple-Input Floating-Gate Transistors", *IEEE Transactions on Circuits and Systems*, vol. 48, no. 1, January 2001, p. 111-116
M	AJ		J. Ramirez-Angulo, *et al.*; "MITE Circuits: The Continuous-Time Counterpart to Switched-Capacitor Circuits", *IEEE Transactions on Circuits and Systems*, vol. 48, no. 1, January 2001, p. 45-55
M	AK		L. Dobrescu, *et al.*; "Threshold Voltage Extraction Methods for MOS Transistors", *IEEE*, 2000, p. 371-374
M	AL		"Slew Rate Control of LVDS Circuits", *Application Report*, Texas Instruments Incorporated, March 1999, p.1-12

EXAMINER	DATE CONSIDERED
	7/10/02

EXAMINER: Initial if reference considered, whether or not citation is in confirmance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.